



	Pin	Signal	Mating Sequence	Pin	Signal	Mating Sequence
	A1	GND	First	B12	GND	First
	A2	SSTXp1	Second	B11	SSRXp1	Second
	А3	SSTXn1	Second	B10	SSRXn1	Second
	A4	V _{BUS}	First	В9	VBUS	First
	A5	CC1	Second	B8	SBU2	Second
	A6	Dp1	Second	В7	Dn2	Second
	A7	Dn1	Second	В6	Dp2	Second
	A8	SBU1	Second	B5	CC2	Second
	A9	VBUS	First	B4	VBUS	First
	A10 SSRXn2		Second	В3	SSTXn2	Second
	A11	SSRXp2	Second	B2	SSTXp2	Second
	A12	GND	First	B1	GND	First
		SHELL	GND		SHELL	GND

Recommended PCB Layout

As viewed from component side Tolerance:±0.05mm

Solder Area [] Component Outline

Part Number			Product Description				Γ	
USB4081			USB3.2 Gen 2 Type C Receptacle, Dual Row SMT, Horizontal, PCB Top Mount					
Drawing Date								
21st De	21st December 2022			Tionzoniai, FCB Top Would				
Ву				Units:		This drawing is confidential and	l	
Detail	Drawing Release	X.X ± 0.30 X.XX ± 0.25	Angle -	Metric (mm)	ROHS	copyright of Global Connector Technology, Ltd (GCT). This drawing must not be copied	L	
Revision	A1				2011/65/EU	or disclosed without written		

Not to Drawn By Scale CC

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